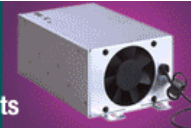


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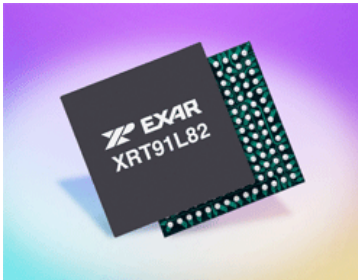
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This article was written for the Sep 2005 issue of Electronic Products

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The XRT91L82 OC-48/STS-48/STM-16 SONET/SDH transceiver provides an industry-standard 16-bit LVDS/LVPECL interface and full-duplex operation at 2.488 Gbits/s, or a forward-error-correction 2.666-Gbit/s rate. The chip includes a clock multiplier, parallel-to-serial and serial-to-parallel converters, and a clock data recovery function.



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